



Sheet 1 of 1

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INFORMATION DISCLOSURE STATEMENT

Applicant: Mark R. Thomann et al.

BY APPLICANT

Filing Date: April 17, 1997

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(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
S-A-C	5,241,546	08/31/1993	Peterson, B.R., et al.	371	38-1	02/01/91
I	5,553,067	09/03/1996	Walker, C.P., et al.	370	60 390	10/07/94
	5,602,828	02/11/1997	Engdahl, T.L., et al.	371	228 20-1	05/26/95
V	5,602,857	02/11/1997	Zook, C.P., et al.	371	40.14	10/18/94

FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
S-A-C	0226353	06/24/1987	European			
S-A-C	3841370	06/22/1989	Germany			
S-A-C	A55120252	09/16/1980	Japan			
S-A-C	A57076621	05/13/1982	Japan			
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S-A-C	WO90/10265	09/07/1990	PCT			

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

S-A-C	"High Speed Parallel cyclic Redundancy Check Generator", <u>IBM Technical Disclosure Bulletin</u> , Vol. 33, No. 5, pp. 51-56, (October 1990)
S-A-C	"Parallel Checksum Calculation Accelerator", <u>IBM Technical Disclosure Bulletin</u> , Vol. 36, No. 12, pp. 647-648, (December 1993)
S-A-C	Johnson, B.W., "Design and Analysis of Fault-Tolerant Digital Systems", <u>Addison-Wesley Series in Electrical and Computer Engineering</u> , pp. 102-113, (1957)

Examiner

Shelly A. Chase

Date Considered

10-16-98 9 3-25-99

*Substitute Disclosure Statement Form (PTO-1449)

**EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.